

**Amendments to the Specification**

Please replace page 18, line 16 through page 19, line 4 with the following:

Referring now to Figure 5, a block diagram illustrating the logical components of an embodiment of the present invention is shown generally as 200. As shown in Figure 5, switch 210 accepts a plurality of inputs from three sources, namely: DCT coefficients 212, EOB Address 214, and Picture Type Bit rate 216. Picture Type Bit rate 216 affects the EOB histogram, as more macroblocks will have larger EOBs since more coefficients will be non-zero at higher bitrates. Based upon the input provided to switch 210, one of a plurality of iDCT algorithms from set 220 is selected. Subset 222 contains iDCT algorithms iDCT\_high and iDCT\_low, based upon the EOB histogram of B-frames in the sequences illustrated in Figure 3. For example, a version of iDCT\_high would be selected for an EOB length of 39 or 50. Similarly a version of iDCT\_low would be selected for an EOB length of 14 or 15. Figure 5 shows two independent choices for each of iDCT\_high and iDCT\_low, resulting in four different customized iDCT algorithms that may be specified for decoding the B-frames of a particular shot. As discussed earlier, iDCT\_AC, iDCT\_DC and iDCT\_Normal are selected based upon the criteria defined by Murata et al